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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Schloesser, et al.	Docket No.:	INF-135
Filed:	2/13/2004	Examiner:	Andy Huynh
Serial No.:	10/777,128	Art Unit:	2818
Title:	Architecture for Vertical Transistor Cells and Transistor-Controlled Memory Cells		

Mail Stop Amendment
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicant wishes to bring to the attention of the U.S. Patent and Trademark Office the information noted on the enclosed PTO Form PTO/SB/08A, which may be considered material to the examination of the above-identified application. A copy of the U.S. Patent cited is not being submitted.

This Information Disclosure Statement is submitted under 37 C.F.R. §1.97(d) together with a Statement under 37 C.F.R. §1.97(e), and a $180.00 fee set forth in 37 C.F.R. §1.17(p) as this disclosure is being filed after notice of allowance, but before payment of the issue fee.

Please charge the required fee of $180.00 and any additional amount, or credit any overpayment to Deposit Acct. No 50-1065 of the below mentioned firm.

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Respectfully submitted,

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FACSIMILE COVER SHEET

To: Commissioner for Patents	Total Pages Sent: 5
Mail Stop Issue Fee	(including cover sheet)
Facsimile Number: 571-273-2885	Transmission Date: December 20, 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Schloesser, et al.	Docket No.:	INF-135
Serial No:	10/777,128	Art Unit:	2818
Date Filed:	February 13, 2004		
Title:	Architecture for Vertical Transistor Cells and Transistor-Controlled Memory Cells		

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at 571-273-2885 on the date shown above:

- Certification of Facsimile Transmission (1 page)
- Information Disclosure Statement (1 original and 1 copy)
- Statement Under 37 CFR 1.97(e) (1 page)
- Form PTO/SB/08A (1 page), with 1 reference cited

Respectfully submitted,

AnneMarie James
Legal Assistant

Confirmation Respectfully Requested

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Schloesser, et al.	
Serial No.: 10/777,128	Examiner: Andy Huynh
Filing Date: 2/13/2004	Group Art Unit: 2818
Title: Architecture for Vertical Transistor Cells and Transistor-Controlled Memory Cells	
Attorney Docket No.: INF-135	

COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

STATEMENT UNDER 37 CFR 1.97(e)

Sir:

The undersigned hereby certifies that either:

(X) Each item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, not more than three months prior to the filing of the statement, or

( ) No item of information contained in the Information Disclosure Statement

-- was cited in a communication from a foreign patent office in a counterpart foreign application, and

-- to the knowledge of the undersigned, after making reasonable inquiry, was known to an individual designated in 37 CFR 1.56 (c) more than three months prior to the filing of the Information Disclosure Statement.

Respectfully Submitted,

By _____
Ira S. Matsil
Attorney/Agent for Applicant
Reg. No.: 35,272

Date: December 20, 2005

Telephone No.: 972-732-1001

I hereby certify that this Correspondence is being deposited with the United States Postal service with sufficient postage for first class mail in an envelope address to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, or the correspondence is being facsimile transmitted to the USPTO, on the date indicated below.

Date of Deposit: December 20, 2005

Typed Name: AnneMarie James

Signature: _____

PTO/SB/08a (08-03)
Approved for use through 07/31/2006. OMB 0651-0031
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1 of 1

Complete if Known	
Application Number	10/777,128
Filing Date	2/13/2004
First Named Inventor	Schloesser, et al.
Art Unit	2818
Examiner Name	Andy Huynh
Attorney Docket Number	INF-135

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	Document Number: Number - Kind Code[2] (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	1	US-5,502,320	03-26-1996	Yamada	
		US-			
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	Foreign Patent Document: Country Code[3] - Number[4] - Kind Code[5] (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T[6]

Examiner Signature		Date Considered	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. [1]Applicant's unique citation designation number (optional). [2]See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. [3]Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). [4]For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. [5]Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. [6]Applicant is to place a check mark here if English language Translation is attached.